

## AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

### Listing of Claims:

- 1        1. (Original) An integrated circuit assembly module, comprising:
  - 2            a first substrate, with a front face and a back face opposite the front face;
  - 3            a first semiconductor die with an active face upon which active circuitry
  - 4            and signal pads reside, and a back face opposite the active face;
  - 5            a second semiconductor die with an active face upon which active circuitry
  - 6            and signal pads reside, and a back face opposite the active face;
  - 7            a second substrate, with a front face and a back face opposite the front
  - 8            face;
  - 9            wherein the first semiconductor die and the second semiconductor die are
  - 10          arranged active face to active face so that signal pads on the first semiconductor
  - 11          die overlap with signal pads on the second semiconductor die, thereby facilitating
  - 12          capacitive communication between the first semiconductor die and the second
  - 13          semiconductor die; and
  - 14          wherein the first semiconductor die and the second semiconductor die are
  - 15          pressed together between the first substrate and the second substrate so that the
  - 16          front face of the first substrate is in contact with the back face of the first
  - 17          semiconductor die and the front face of the second substrate is in contact with the
  - 18          back face of the second semiconductor die.

1        2. (Original) The integrated circuit assembly module of claim 1, further  
2 comprising:

3            a first heat removal mechanism coupled to the back face of the first  
4 substrate; and

5            a second heat removal mechanism coupled to the back face of the second  
6 substrate.

1        3. (Original) The integrated circuit assembly module of claim 2, wherein  
2 the first and second heat removal mechanisms include cooling fins to facilitate the  
3 transfer of heat to air passing across the cooling fins.

1        4. (Original) The integrated circuit assembly module of claim 2, wherein  
2 the first and second heat removal mechanisms include graphite foam or metal with  
3 fin-like structures which facilitate in the transfer of heat to a liquid pumped  
4 through the graphite foam or metal.

1        5. (Original) The integrated circuit assembly module of claim 1, further  
2 comprising:

3            a first power supply coupled to the back face of the first substrate; and  
4            a second power supply coupled to the back face of the second substrate.

1        6. (Original) The integrated circuit assembly module of claim 1, wherein  
2 the first and second substrates include metal layers that supply power to the first  
3 and second semiconductor dies.

1        7. (Original) The integrated circuit assembly module of claim 6, wherein  
2        the first and second substrates include power connectors on the back faces of the  
3        first and second substrates.

1        8. (Original) The integrated circuit assembly module of claim 6, further  
2        comprising Micro Electro-Mechanical System (MEMS) spring contacts that  
3        provide power from the metal layers within the first and second substrates to the  
4        first and second semiconductor dies, wherein:

5            a first set of MEMS spring contacts on the front face of the first substrate  
6        contact the active side of the second semiconductor die; and  
7            a second set of MEMS spring contacts on the front face of the second  
8        substrate contact the active side of the first semiconductor die.

1        9. (Original) The integrated circuit assembly module of claim 6, further  
2        comprising:  
3            a first power regulator incorporated within the first substrate; and  
4            a second power regulator incorporated within the second substrate.

1        10. (Original) The integrated circuit assembly module of claim 1, further  
2        comprising:  
3            an I/O semiconductor die with an active face upon which active circuitry  
4        and signal pads reside, and a back face opposite the active face;  
5            wherein the I/O semiconductor die and the second semiconductor die are  
6        arranged active face to active face, so that signal pads on the I/O semiconductor  
7        die overlap with signal pads on the second semiconductor die, thereby facilitating  
8        capacitive communication between the I/O semiconductor die and the second  
9        semiconductor die;

10       wherein the I/O semiconductor die is located on an edge of the first  
11   substrate to facilitate in providing communications into and out of the integrated  
12   circuit assembly module; and

13       wherein the edge of the first substrate extends beyond the edge of the  
14   second substrate so that a portion of the active face of the I/O semiconductor die is  
15   exposed to facilitate external connections.

1       11. (Original) The integrated circuit assembly module of claim 10, further  
2   comprising optical external connection pads located on the exposed portion of the  
3   active side of the I/O semiconductor die.

1       12. (Original) The integrated circuit assembly module of claim 10, further  
2   comprising electrical external connection pads located on the exposed portion of  
3   the active side of the I/O semiconductor die.

1       13-24. (Canceled)

1       25. (Original) A computer system comprising an integrated circuit  
2   assembly module, comprising:  
3       a first substrate, with a front face and a back face opposite the front face;  
4       a first semiconductor die with an active face upon which active circuitry  
5   and signal pads reside, and a back face opposite the active face;  
6       a second semiconductor die with an active face upon which active circuitry  
7   and signal pads reside, and a back face opposite the active face;  
8       a second substrate, with a front face and a back face opposite the front  
9   face;

10       wherein the first semiconductor die and the second semiconductor die are  
11   arranged active face to active face so that signal pads on the first semiconductor  
12   die overlap with signal pads on the second semiconductor die, thereby facilitating  
13   capacitive communication between the first semiconductor die and the second  
14   semiconductor die; and

15       wherein the first semiconductor die and the second semiconductor die are  
16   pressed together between the first substrate and the second substrate so that the  
17   front face of the first substrate is in contact with the back face of the first  
18   semiconductor die and the front face of the second substrate is in contact with the  
19   back face of the second semiconductor die.

1       26. (Original) The computer system of claim 25, wherein the integrated  
2   circuit assembly module further comprises:

3           a first heat removal mechanism coupled to the back face of the first  
4   substrate; and

5           a second heat removal mechanism coupled to the back face of the second  
6   substrate.

1       27. (Original) The computer system of claim 26, wherein the first and  
2   second heat removal mechanisms include cooling fins to facilitate the transfer of  
3   heat to air passing across the cooling fins.

1       28. (Original) The computer system of claim 26, wherein the first and  
2   second heat removal mechanisms include graphite foam or metal with fin-like  
3   structures which facilitate in the transfer of heat to a liquid pumped through the  
4   graphite foam or metal.

1        29. (Original) The computer system of claim 25, further comprising:  
2            a first power supply coupled to the back face of the first substrate; and  
3            a second power supply coupled to the back face of the second substrate.

1        30. (Original) The computer system of claim 25, wherein the first and  
2        second substrates include metal layers that supply power to the first and second  
3        semiconductor dies.

1        31. (Original) The computer system of claim 30, wherein the first and  
2        second substrates include power connectors on the back faces of the first and  
3        second substrates.

1        32. (Original) The computer system of claim 30, wherein the integrated  
2        circuit assembly module further comprises Micro Electro-Mechanical System  
3        (MEMS) spring contacts that provide power from the metal layers within the first  
4        and second substrates to the first and second semiconductor dies, wherein:  
5            a first set of MEMS spring contacts on the front face of the first substrate  
6        contact the active side of the second semiconductor die; and  
7            a second set of MEMS spring contacts on the front face of the second  
8        substrate contact the active side of the first semiconductor die.

1        33. (Original) The computer system of claim 30, wherein the integrated  
2        circuit assembly module further comprises:  
3            a first power regulator incorporated within the first substrate; and  
4            a second power regulator incorporated within the second substrate.

1       34. (Original) The computer system of claim 25, wherein the integrated  
2 circuit assembly module further comprises:

3           an I/O semiconductor die with an active face upon which active circuitry  
4 and signal pads reside, and a back face opposite the active face;

5           wherein the I/O semiconductor die and the second semiconductor die are  
6 arranged active face to active face, so that signal pads on the I/O semiconductor  
7 die overlap with signal pads on the second semiconductor die, thereby facilitating  
8 capacitive communication between the I/O semiconductor die and the second  
9 semiconductor die;

10          wherein the I/O semiconductor die is located on an edge of the first  
11 substrate to facilitate in providing communications into and out of the integrated  
12 circuit assembly module; and

13          wherein the edge of the first substrate extends beyond the edge of the  
14 second substrate so that a portion of the active face of the I/O semiconductor die is  
15 exposed to facilitate external connections.

1       35. (Original) The computer system of claim 34, wherein the integrated  
2 circuit assembly module further comprises optical external connection pads  
3 located on the exposed portion of the active side of the I/O semiconductor die.

1       36. (Original) The computer system of claim 34, wherein the integrated  
2 circuit assembly module further comprises electrical external connection pads  
3 located on the exposed portion of the active side of the I/O semiconductor die.